



Microprocessor IP Core Based on ARM7

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TOOLS:

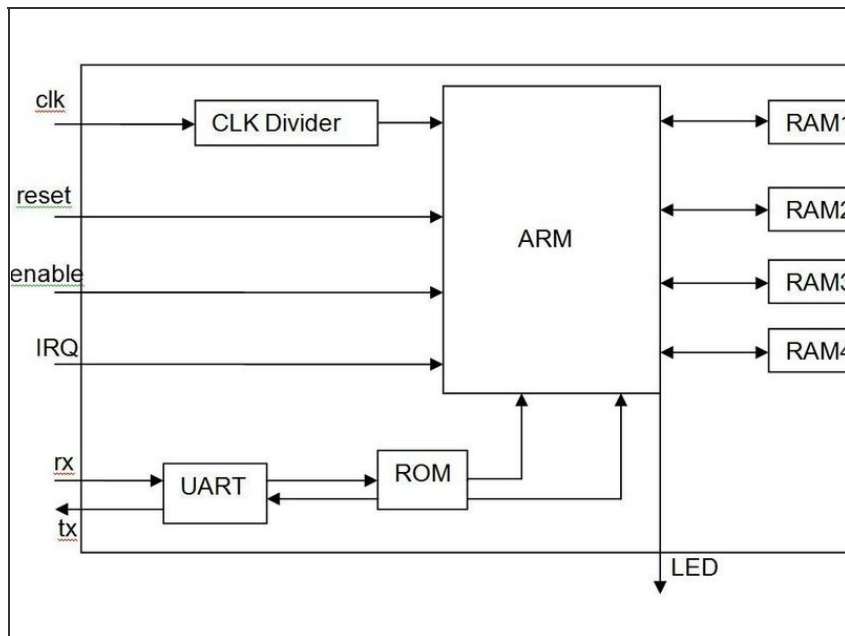
- [Xilinx ISE \(1\)](#)



PARTS:

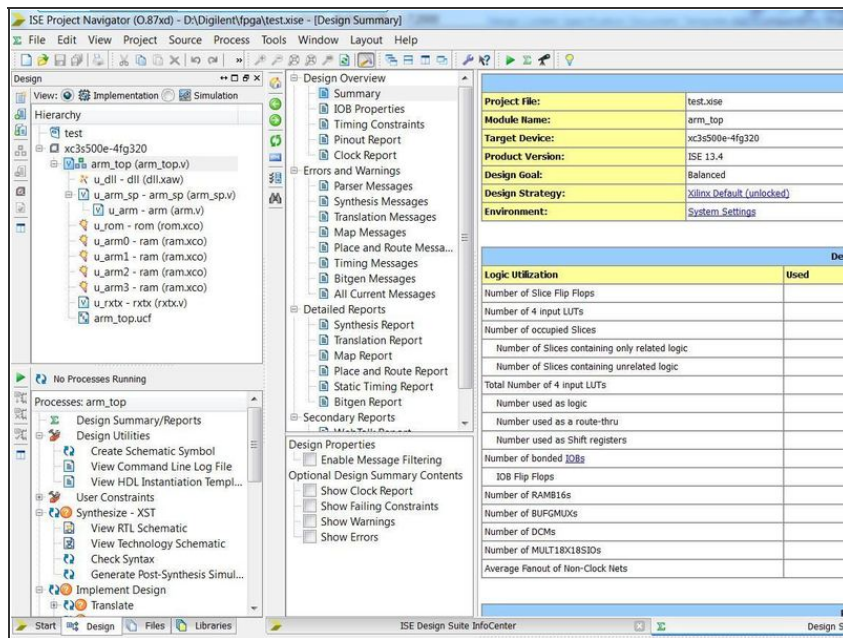
- [FPGA development board \(1\)](#)

Step 1 — Microprocessor IP Core Based on ARM7



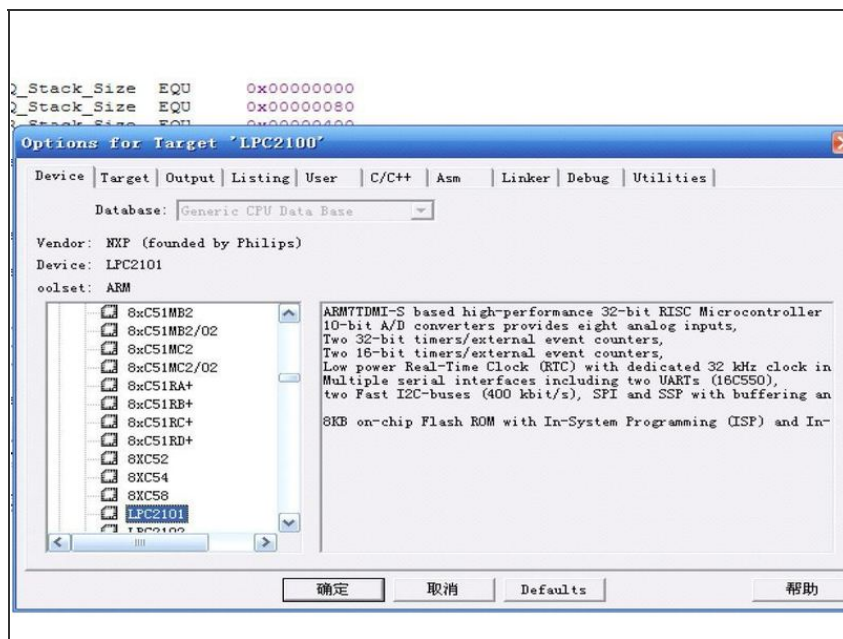
- Draw the block diagram of the microprocessor.

Step 2



- Implement in Xilinx ISE.

Step 3



- Generate sample program for testing in Keil. Change target device to LPC2101.

Step 4

- Compile the program to get a hex file, then convert hex file to bin file. Many software packages can do the conversion.

Step 5

```
Hello World This is      1 time.  
Hello World This is      2 time.  
Hello World This is      3 time.  
Hello World This is      4 time.  
Hello World This is      5 time.  
Hello World This is      6 time.  
Hello World This is      7 time.  
Hello World This is      8 time.  
|
```

- Run the program on FPGA and check debugging information in hyperterminal.

This document was last generated on 2012-11-03 04:03:25 AM.